

Electronic Patent Application Fee Transmittal

| | | | | |
|---|--|-----------------|---------------|-----------------------------|
| Application Number: | | | | |
| Filing Date: | | | | |
| Title of Invention: | Method and Device For Secure, Insulated and Electrically Conductive Assembling of Treated Semiconductor Wafers | | | |
| First Named Inventor: | Roy KNETCHTEL | | | |
| Filer: | Rodger L. Tate/Mildred Ayim | | | |
| Attorney Docket Number: | 60291.000048 | | | |
| Filed as Large Entity | | | | |
| U.S. National Stage under 35 USC 371 Filing Fees | | | | |
| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
| Basic Filing: | | | | |
| National Stage Fee | 1631 | 1 | 300 | 300 |
| Natl Stage Search Fee - Report provided | 1642 | 1 | 400 | 400 |
| National Stage Exam - all other cases | 1633 | 1 | 200 | 200 |
| Pages: | | | | |
| Claims: | | | | |
| Miscellaneous-Filing: | | | | |
| Eng. Transl. > 30 mo. from priority date | 1618 | 1 | 130 | 130 |
| Petition: | | | | |